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Confirmation Number:	7330
Title of Invention:	Interface between digital and analog circuits
First Named Inventor/Applicant Name:	Gurkanwal Sahota
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	990307C1_OA_Response-03-23-09.pdf	115023 0b31f68056a9d0d7ffc229429f3ada1e6b4b5a27	no	11
Warnings:					
Information:					

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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